

In re the Application of:

TI-31779

Shakuntala Anjanaiah, et al

Art Unit: 2183

Serial No:

09/964,315

Examiner:

Filed:

For:

September 26, 2001

Apparatus and Method for An Interface Unit for Data Transfer Between Processing Units in the Asynchronous Transfer Mode

## LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Ass't Commissioner for Patents, Washington, D.C.

William W. Holloway, Reg. No. 26,182

Ass't Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

Please find enclosed the formal drawings for the subject case. Charge any necessary fees to Deposit Account No. 20-0668. This form is submitted in triplicate.

Respectfully submitted,

William W. Holloway Attorney for Applicants

Reg. No. 26,182

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (281) 274-4064

RECEIVED

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**OFFICE OF PETITIONS** 

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100 120 16~ HOST PROCESSOR INTERFACE UNIT 14 **CENTRAL EXTERNAL** PROCESSING I **PROCESSING** DIRECT **ASYNCHRONOUS** UNIT UNIT **MEMORY** TRANSFER MODE **ACCESS UNIT** INTERFACE UNIT 10 ~110 18 MEMORY UNIT -12 10N 16-HOST PROCESSOR INTERFACE UNIT 14 **CENTRAL PROCESSING DIRECT ASYNCHRONOUS** UNIT **MEMORY** TRANSFER MODE INTERFACE UNIT **ACCESS UNIT** 10 18 **MEMORY UNIT** -12

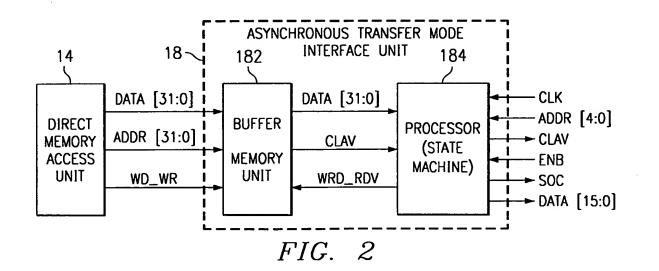
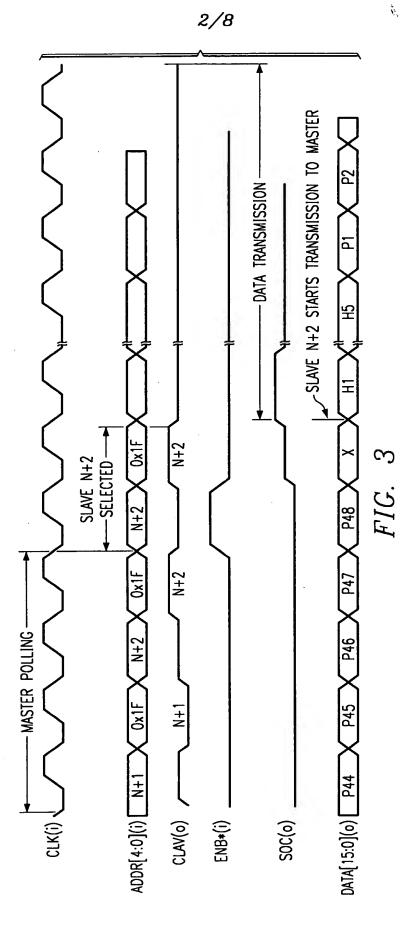
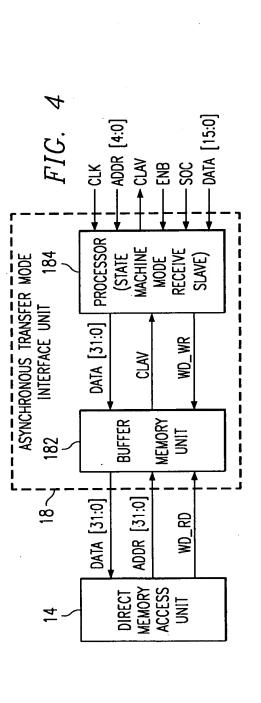
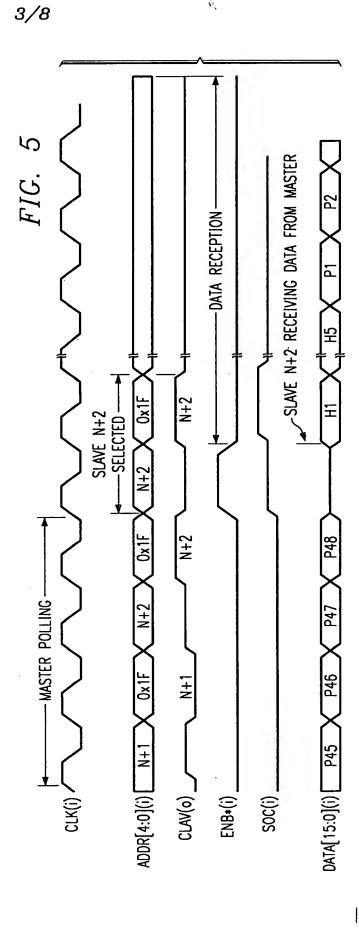


FIG.







v.



FIG.

184

ASYNCHRONOUS TRANSFER MODE
R2 INTERFACE UNIT 184

182

-7 @E

► ADDR [4:0]

**PROCESSOR** 

DATA [31:0]

DATA [31:0]

-CK

-CLAV

(MASTER-TRANSMIT STATE

CLAV [31:0]

BUFFER

ADDR [31:0]

MEMORY ACCESS

DIRECT

MEMORY

LINO

WD\_WR

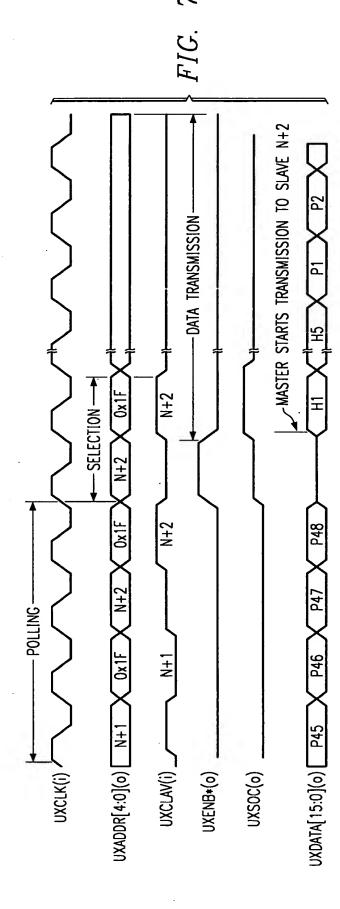
LINO LINO

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MACHINE)

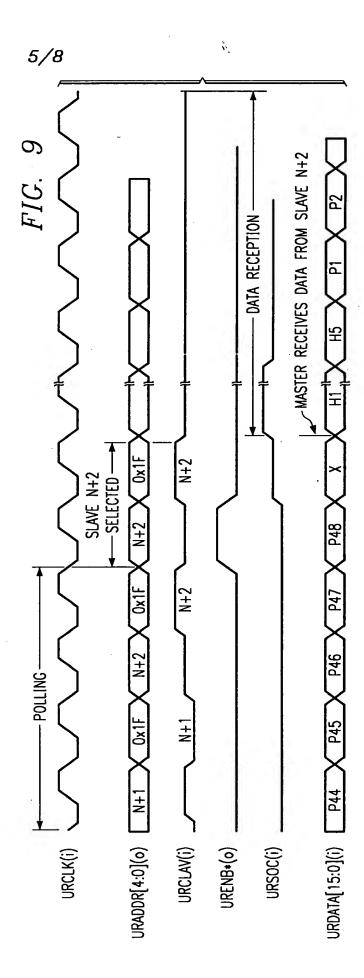
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 $\varphi$ DATA [15:0] FIG.► ADDR [4:0] -CLAV **₽**ENB . S0C (MASTER-RECEIVE STATE **PROCESSOR** MACHINE) 184 ASYNCHRONOUS TRANSFER MODE
R2 INTERFACE UNIT 184 DATA [31:0] WD\_WR <u>₹</u> MEMORY BUFFER IN N 182 ~ ₩ DATA [31:0] ADDR [31:0] WD\_RD DIRECT MEMORY ACCESS UNIT

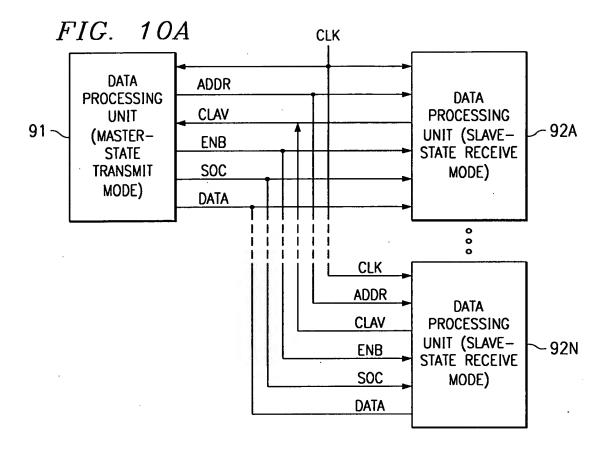


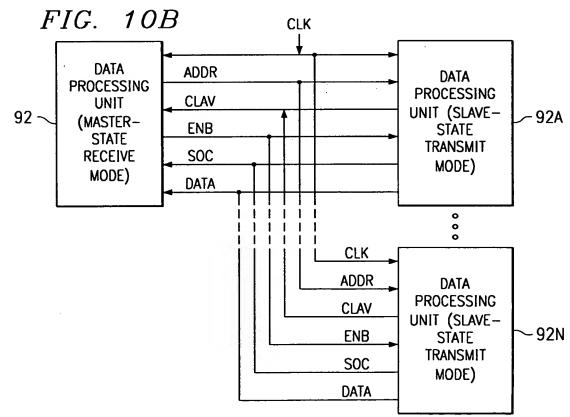




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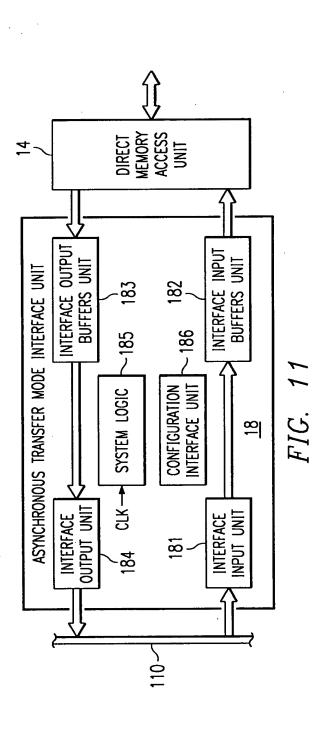






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_	· · · · · ·		
16	UXEN	0	UREN
17	UXMSTR UXEN	1	URMSTR UREN
18	XUDC	2	RUDC
19		3	
20 19		4	RU
21		5	
22	Rsv	9	RSV
23		7	
24	SLID/SLEND	80	Rsv
25		6	
56		10	>
27		11	Rsv
28		12	NPM
53	Rsv	13	U16M
30	Rsv	14	МРНУ
31	BEND	15	ULB

F.I.G. 12

